

## CLAIMS

What is claimed is:

1. An odometer counter, said odometer counter comprising:  
an encoder, said encoder having a unique key, said key constructed and arranged to configure  
an encryption engine, said encoder further constructed and arranged to execute said encryption  
engine with an odometer value to form an encrypted odometer value, said encoder further  
constructed and arranged to store said odometer value in a non-volatile memory; and  
a decoder, said decoder constructed and arranged to receive said encrypted odometer value  
from said encoder, said decoder further constructed and arranged to decrypt said encrypted odometer  
value and to transmit said decrypted odometer value.

2. An odometer counter as in claim 1 wherein said encoder is further constructed and  
arranged to increment said odometer value upon receiving an increment signal.

3. An odometer counter as in claim 1 wherein said key is 64 bits in length.

4. An odometer counter as in claim 1 wherein odometer value is 16 bits in length.

5. An odometer counter as in claim 4 wherein said 16 bit odometer value is combined  
with another 16 bit value.

6. An odometer counter as in claim 5 wherein said another 16 bit value is composed of

2 a pre-defined 12 bit value and a 4 bit value based upon inputs to said encoder.

a) 1 7. An odometer counter as in claim 6 wherein said 12 bit value is stored in said non-  
2 volatile memory of said encoder.

1 8. An odometer counter as in claim 1 wherein said data packet from said encoder is  
2 transmitted asynchronously to said decoder.

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1 9. An method for securing odometer values, said method comprising the steps of:  
2 (a) providing an encoder having a unique key and an encryption engine;  
3 (b) configuring said encryption circuit with said key;  
4 (c) receiving an increment signal;  
5 (d) incrementing an odometer value to form an incremented odometer value;  
6 (e) storing said incremented odometer value into said a non-volatile memory;  
7 (e) encrypting said incremented odometer value with said encryption engine to form an  
8 encrypted odometer value;  
9 (f) transmitting said encrypted odometer value to a decoder; and  
10 (g) decrypting said encrypted odometer value with said decoder to obtain said odometer  
11 value.

1 10. A method as in claim 9, wherein said step of encrypting said odometer value with said  
2 encryption engine to form an encrypted odometer value comprises the following steps:

3 (e1) adding a 16-bit value to said odometer value to form a 32 bit value; and

4 (e2) encrypting said 32 bit value to form said encrypted odometer value.

1 11. A method as in claim 9, wherein said step of transmitting said encrypted odometer  
2 value to a decoder comprises the following steps:

3 (f1) generating a data packet with said encrypted odometer value;

4 (f2) adding an identifier code to said data packet to form a tagged data packet; and.

(f3) transmitting said tagged data packet from said encoder to said decoder.

12. An odometer counter as in claim 6 wherein a portion of said 12 bit value is stored in  
said non-volatile memory of said encoder.

13. An odometer counter as in claim 6 wherein a portion of said 12 bit value is a  
checksum based on said odometer value.

1 14. A counter, said counter comprising:  
2 an encoder, said encoder having a unique key, said key constructed and arranged to configure  
3 an encryption engine, said encoder further constructed and arranged to execute said encryption  
4 engine with an value to form an encrypted value, said encoder further constructed and arranged to  
5 store said value in a non-volatile memory; and

6 a decoder, said decoder constructed and arranged to receive said encrypted value from said  
7 encoder, said decoder further constructed and arranged to decrypt said encrypted value and to  
8 transmit said decrypted value.

1 15. A counter as in claim 14 wherein said encoder is further constructed and arranged to  
2 increment said value upon receiving an increment signal.

1 16. A counter as in claim 14 wherein said key is 64 bits in length.

17. A counter as in claim 14 wherein value is 16 bits in length.

18. A counter as in claim 17 wherein said 16 bit value is combined with another 16 bit  
value.

19. A counter as in claim 18 wherein said another 16 bit value is composed of a pre-  
defined 12 bit value and a 4 bit value based upon inputs to said encoder.

1 20. A counter as in claim 19 wherein said 12 bit value is stored in said non-volatile  
2 memory of said encoder.

1 21. A counter as in claim 14 wherein said data packet from said encoder is transmitted  
2 asynchronously to said decoder.

1 22. An method for securing values, said method comprising the steps of:  
2 (a) providing an encoder having a unique key and an encryption engine;  
3 (b) configuring said encryption circuit with said key;  
4 (c) receiving an increment signal;  
5 (d) incrementing an value to form an incremented value;  
6 (e) storing said incremented value into said a non-volatile memory;  
7 (e) encrypting said incremented value with said encryption engine to form an encrypted  
8 value;  
9 (f) transmitting said encrypted value to a decoder; and  
10 (g) decrypting said encrypted value with said decoder to obtain said value.

11 23. A method as in claim 22, wherein said step of encrypting said value with said  
12 encryption engine to form an encrypted value comprises the following steps:

- 13 (e1) adding a 16-bit value to said value to form a 32 bit value; and  
14 (e2) encrypting said 32 bit value to form said encrypted value.

15 24. A method as in claim 22, wherein said step of transmitting said encrypted value to  
16 a decoder comprises the following steps:

- 17 (f1) generating a data packet with said encrypted value;  
18 (f2) adding an identifier code to said data packet to form a tagged data packet; and.  
19 (f3) transmitting said tagged data packet from said encoder to said decoder.

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25. A counter as in claim 6 wherein a portion of said 12 bit value is stored in said non-volatile memory of said encoder.

26. A counter as in claim 19 wherein a portion of said 12 bit value is a checksum based on said value.

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